

Docket No.: M4065.0570/P570-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard Rhodes et al.

Group Art Unit: 2811

Application No.: 10/660,565

Examiner: Andrew Owens Arena

Filed: September 12, 2003

Confirmation No.: 5308

For: METHOD OF FORMING CMOS IMAGER
PIXELS WITH CAPACITOR STRUCTURES
(AS AMENDED)

Allowed: December 30, 2008

**COMMENTS ON EXAMINER'S
STATEMENT OF REASONS FOR ALLOWANCE**

MS Issue Fee
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants agree in part with the Examiner's statement of reasons for allowance in that the prior art of record fails to anticipate or render obvious the claimed invention. However, Applicants note that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's reasons for allowance, including those in the dependent claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.


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Applicants submit that the scope of the claims should be interpreted based on the actual language of the allowed claims, and no further limitation of the claims should be inferred from the Examiner's Statement of Reasons For Allowance.

Dated: March 30, 2009

Respectfully submitted,

By 

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